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EXPERIMENT #6: XOR IMPLEMENTATION AND PARITY CODE

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EXPERIMENT #10: FLIP-FLOPS AND THEIR APPLICATIONS

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ICS 413: Lab Manual

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1. CALIBRATION OF LVDT TRANSDUCER FOR DISPLACEMENT ...

EE 200 Lab Manual, EE Department, KFUPM Lab Work: 4-bit Register 1. Connect the 4-bit register (as shown in Figure 4) on the proto-board using two 7474 ICs. a. Connect the D inputs (I0, I1, I2, and I3) of the four D FFs to four DIP switches. b. Connect PRESET inputs of all flip-flops together. c. Connect CLEAR inputs of all flip-flops together. d.

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EE 200 Lab Manual, EE Department, KFUPM Lab Work: 1. Implement the sequential circuit (you designed in the pre-lab) on the proto-board using IC 7476 which has two JK-type Flip-flops and any external gates required. a. Connect PRESET and CLEAR inputs to logic-1. b. Connect CLK input to a pulser-button.

Electrical - Undergraduate Courses

Lab Tasks. A space agency wants to develop a database of all satellites that humans have launched into space. Data includes the satellite identification, date of launch, destruction date, purpose, maximum orbital altitude, launching location, launching agency, and contact information for agency.

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This course provides a comprehensive and current introduction to computer networks with focus on the functions performed at each layer of the network architecture and common layer protocol standards.

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King Fahd University of Petroleum & Minerals MECHANICAL ENGINEERING DEPARTMENT ME 413: System Dynamics and Control Catalogue Description: (2-3-3) Dynamics of mechanical, fluid, electrical and thermal systems. Equations of motion. Dynamic response of elementary systems. Transfer functions and pole-zero diagrams.

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ICS Lab manual housing and the end leads provides electrostatic and electromagnetic shielding. The frequency of AC applied to primary windings may be between 50 Hz to 20 kHz. Since the primary winding is excited by an alternating source, it produces an alternating

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Lab Manual; Download Lab Manual [EE204_Lab_Manual.pdf] EE 205 - Electric Circuits II (3-0-3) Analysis of three-phase networks. Time domain solutions of second order circuits. State equations for linear circuits. Computer-aided circuit analysis. Frequency domain analysis and Bode plots. Network analysis in the S-domain. Mutual inductance and ...

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EXPERIMENT #11: DESIGN OF SEQUENTIAL CIRCUITS

EE 200 Lab Manual, EE Department, KFUPM Part 2: For the circuit shown in Figure 3, perform the following tasks: Pre-lab Work: (All Pre-lab work must be shown in the Pre-lab report) 1. Obtain the Boolean expression for F. 2. Obtain the truth table for F. 3. Simplify the function F to only four literals. 4. Draw the logic diagram of the simplified

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ICS 431 Operating System Lab Lab # 2 Introduction to C language Objectives: ... Lab Problems will be given during the lab based on material covered in this lab manual. Title: King Fahd University of Petroleum and Minerals

EXPERIMENT #2: LOGIC GATES AND BOOLEAN ALGEBRA

EE 200 Lab Manual, EE Department, KFUPM Pre-lab Work: 1. Obtain the truth table for 4-bit Parity Generator function Pg. 2. Implement function Pg using minimum number of XOR gates. Draw the logic diagram. 3. Obtain truth table for 5-bit Parity Checker function PC. 4. Implement function PC using minimum number of XOR gates. Draw the logic diagram. 5.

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The Information and Comp uter Science (ICS) D epartment at KFUPM was establis hed in September 1979 and it developed and evolved over the years to become one of the active departments of the University in teaching, research, and service to the university.