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INTRODUCTION TO DIGITAL SYSTEMS

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digital logic circuits and computer systems. The role of modeling and simulation in the optimization and verification of digital system design at the

transistor level is described. Chapter 6 provides graphical means and Karnaugh map to streamline and simplify digital system design using visualization schemes.

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Short biography: Professor Ercegovac earned his PhD ('75) and MS ('72) in computer science from the University of Illinois, Urbana-Champaign, and BS in electrical engineering ('65) from the University of Belgrade, Serbia. He specializes in research and teaching in digital arithmetic, digital design, and computer system architecture.

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