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HSPICE® Simulation and Analysis User Guide Version X-2005.09, September 2005

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[1] Spectre User Simulation Guide, pages 160-165 [2] M. Tian, V. Viswanathan, J. Hangtan, K. Kundert, "Striving for Small-Signal Stability: Loop-based and Device-based Algorithms for Stability Analysis of Linear Analog Circuits in the Frequency Domain," Circuits and Devices, Jan 2001.

### **Affirma Spectre Circuit Simulator User Guide**

To see how the Spectre circuit simulator is run under the analog circuit design environment, read the Cadence Analog Design Environment User Guide. For more information about using the Spectre circuit simulator with SpectreHDL, see the SpectreHDL Reference manual. For more information about using the Spectre circuit simulator with Verilog-A ...

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