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separation, there are two groups of filter banks: multi-channel or M-channel filter banks ($M > 2$) and two-channel filter banks: $M = 2$. • In the first group, the signal is separated into M different channels and in the second group into two channels. • We start with the two-channel case.

Multirate Systems And Filter Banks

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Vaidyanathan's book is a very concise, yet enjoyable book on multirate systems and filter banks. Multirate systems and Filter banks represent some of the state-of-the-art research even today, and I'm a strong proponent of introducing the basic concepts as early as possible, even in the first DSP course.

Multidimensional Multirate Systems - Wikipedia

Yuan-Pei Lin, See-May Phoong, and P. P. Vaidyanathan, Filter bank transceivers for OFDM and DMT systems, Cambridge University Press, 2010. Theses ; P. P. Vaidyanathan, ``Some algorithms for digital signal processing," Masters' dissertation, Institute of Radiophysics and Electronics, University of Calcutta, India, 1979.

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Basic Multi-rate Operations: Decimation and Interpolation Building blocks for traditional single-rate digital signal processing: multiplier (with a constant), adder, delay, multiplier (of 2 signals) New building blocks in multi-rate signal processing: M-fold decimator L-fold expander Readings: Vaidyanathan Book x4.1: tutorial Sec. II A, B

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Multirate digital filters, filter banks, polyphase ...

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Basic Building Blocks of MD Multirate Systems. • Expansion (Up-sampling): The L times up sampled version of $x(n)$ defined as $Y(n) = x(L^{-1} \cdot n)$, where n is in the range of lattice generated by L which is $L \cdot m$. The matrix L is called expansion matrix.

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gle-rate system, the sample spacing in a multirate system can vary from point to point [1], [2]. This often results in more efficient processing of signals because the sampling rates at various internal points can be kept as small as possible. Unfortunately, this also results in the introduction of a new

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synthesis-analysis system is used, as shown in Figure 2. This system is also called a transmultiplexer. In this system, all the M signals are interpolated by a factor of M and filtered by M synthesis filters $F_k(z)$ for $k = 0, 1, \dots, M-1$. Then, the outputs are added to give a single signal with sampling rate being M times that of the input signals.

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P. P. Vaidyanathan is a Professor in Electrical Engineering at the California Institute of Technology, where he has been a faculty member since 1983. He is an IEEE Fellow and has authored over 400 technical papers and invited chapters in leading journals, conferences, and handbooks.

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